

**ABSTRACT OF THE DISCLOSURE**

An array substrate for an in-plane switching liquid crystal display device includes a substrate, a gate line and a common line on the substrate, the gate and common lines disposed parallel to and spaced apart from each other, a gate insulator on the gate and common lines, and a data line substantially perpendicular to the gate and common lines on the gate insulator such that a pixel region defined by the gate and data lines. The pixel region is divided into first and second pixel region by the common line. A thin film transistor is adjacent to a crossing portion of the gate and data lines, the thin film transistor having a drain electrode. A first storage electrode is on the gate insulator over the common line, and the first storage electrode connected to the drain electrode. A second storage electrode is on the gate insulator over the gate line, and the second storage electrode connected to the first storage electrode. A common electrode is connected to the common line and has a plurality of extended portions at the first pixel region perpendicular to the common line. A first pixel electrode at the first pixel region is connected to the drain electrode and has a plurality of extended portions perpendicular to the common line. The plurality of extended portions of first pixel electrode alternate with the plurality of extended portions of common electrode.